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L6 and revers\$	8

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L7

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<u>L7</u>	L6 and revers\$	8	<u>L7</u>
<u>L6</u>	L5 and (buffer\$ or cache\$)	10	<u>L6</u>
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<u>L1</u>	(386/1  386/27  386/33  386/46  386/105  386/106  386/109  386/111  386/112  386/124  386/126  386/52 OR 375/240.12  375/240.13  375/240.14  375/240.15  375/240.16  375/240 OR 348/715  348/714  348/718  348/578  348/699).CCLS.	5333	<u>L1</u>